



Docket No. 51992-003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Karl M. Bizjak :
Serial No.: 09/726,983 : Group Art Unit: Unknown
Filed: November 29, 2000 : Examiner: Unknown
For: COMPANDER ARCHITECTURE & METHOD

PRELIMINARY AMENDMENT

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Pursuant to the Notice to File Corrected Application Papers and prior to examination of the above-referenced application, please amend the application as follows; this amendment does not introduce any new matter to the application as filed.

IN THE SPECIFICATION:

1. Please replace the paragraph at page 18, paragraph 2, with the following rewritten paragraph:

-- Figures 1A-1E show prior art signal processing systems. --

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2. Please amend the specification by adding the following abstract as page 156:

-- ABSTRACT:

Comander architecture and method for ensuring that the components of an output signal are synchronized includes calculating a gain calculation for an input signal, detecting a predetermined condition such as a zero crossing or absence of a zero crossing within a specified period, and ensuring that the result of the gain calculation is providing to the system synchronously with the input signal. The input signal may be divided into one or more signals for processing, and the gain calculation may include one or more power estimations and one or more gain cells associated with the multiple signals, and may further include variable attack and release. --

REMARKS

Applicant herewith submits the proposed Preliminary Amendment in order to correct a typographical error in the initial application pertaining to the number of the figures, as filed November 29, 2000. Further, Applicant herewith amends the application by adding an abstract which was omitted from the initial filing.

Attached hereto is a marked-up version of the changes made to the specification by the current amendment. The attached page is captioned "Version with Markings to Show Changes Made."



Applicant respectfully requests that this application proceed to examination.

Respectfully submitted,

McDERMOTT, WILL & EMERY

A handwritten signature in cursive script, reading "Leonard T. Guzman".

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Specification

On page 18, paragraph 2, has been amended as follows:

Figures 1A-[1F] 1E show prior art signal processing systems.

In the Abstract

Page 156, the abstract has been added as follows:

ABSTRACT

Compander architecture and method for ensuring that the components of an output signal are synchronized includes calculating a gain calculation for an input signal, detecting a predetermined condition such as a zero crossing or absence of a zero crossing within a specified period, and ensuring that the result of the gain calculation is providing to the system synchronously with the input signal. The input signal may be divided into one or more signals for processing, and the gain calculation may include one or more power estimations and one or more gain cells associated with the multiple signals, and may further include variable attack and release.